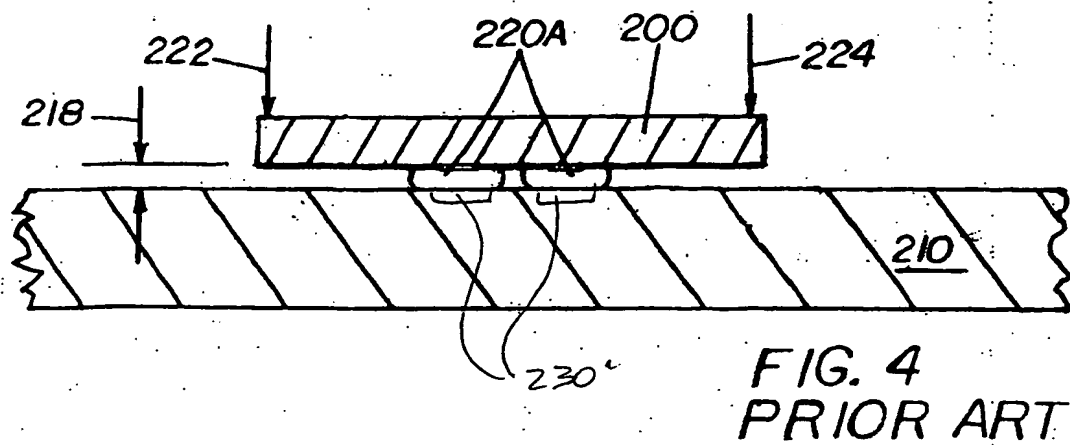
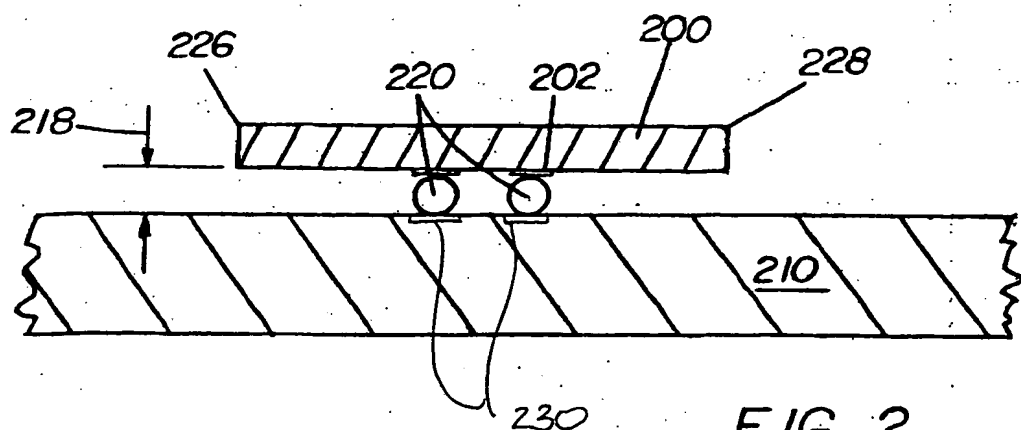
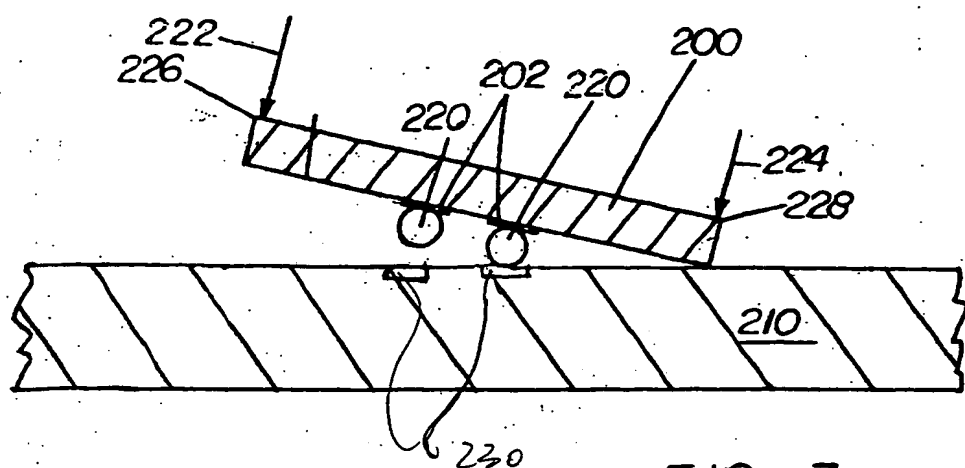


FIG. 1
PRIOR ART

FIG. 1
PRIOR ART

22 Figs.



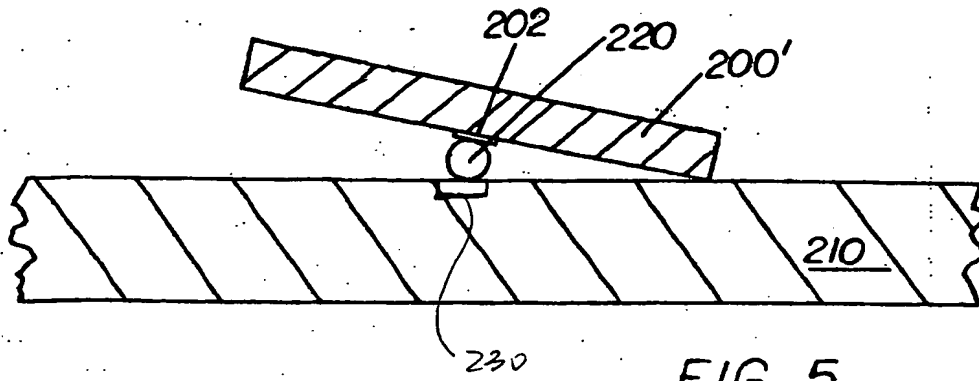


FIG. 5
PRIOR ART

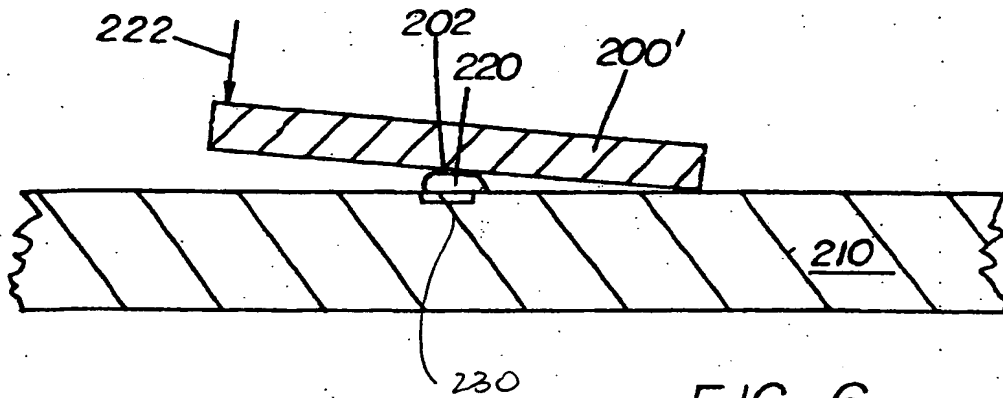


FIG. 6
PRIOR ART.

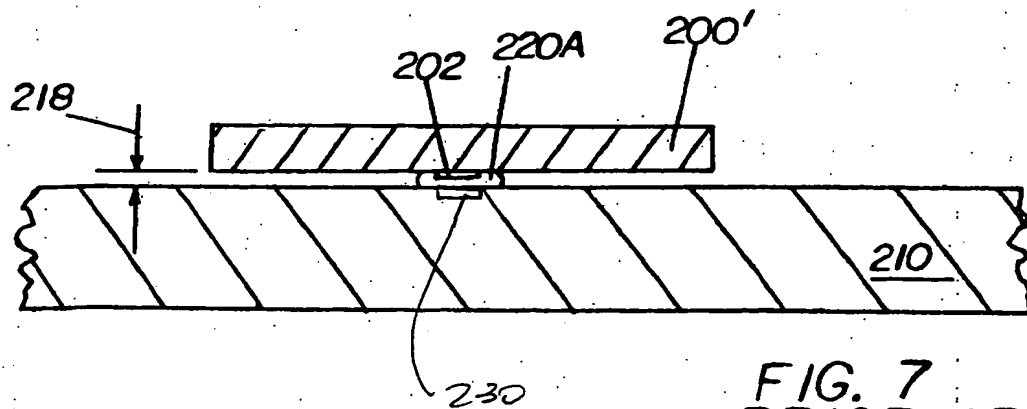


FIG. 7
PRIOR ART

000001.000000

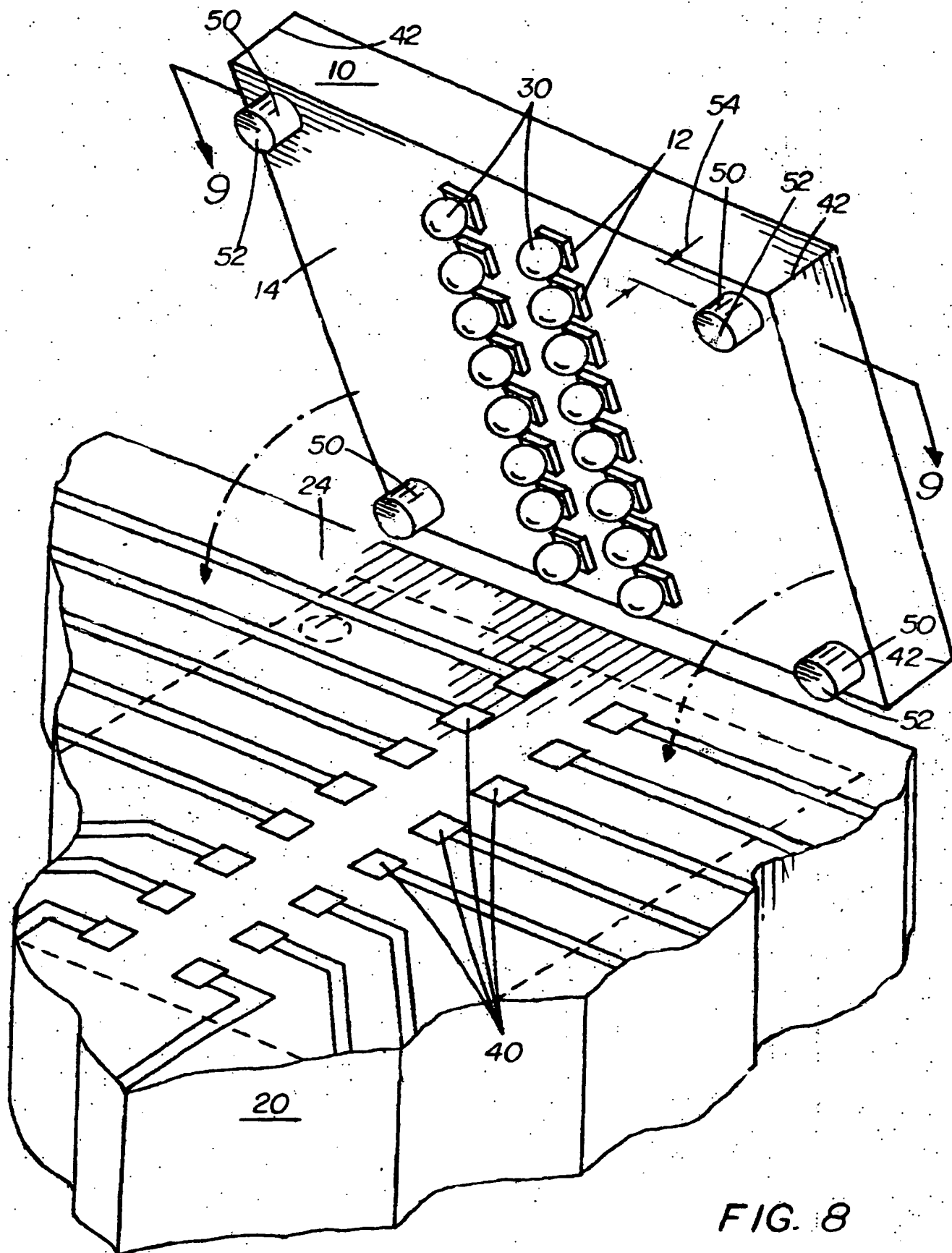
[illegible]

FIG. 8

This diagram is a cross-sectional view of a semiconductor device. It shows a substrate 10 with a layer 12 on top. A layer 14 is positioned between two circular features 30A and 30B. A layer 20 is located below the substrate 10. Various regions are labeled with numbers: 50, 52, 54, 54A, 54B, 58A, 58B, 60, and 28. A dashed line 40 is also shown.

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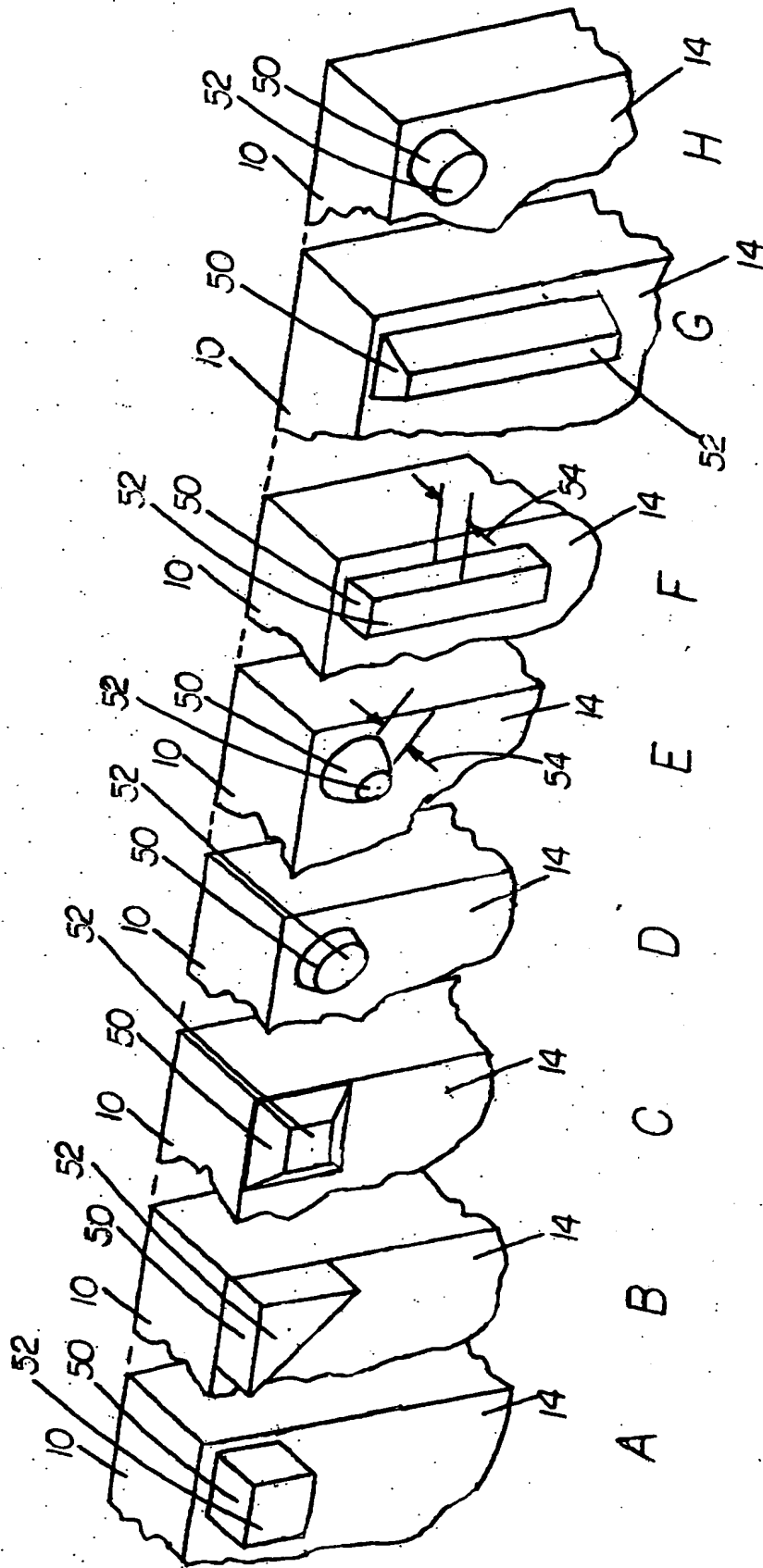
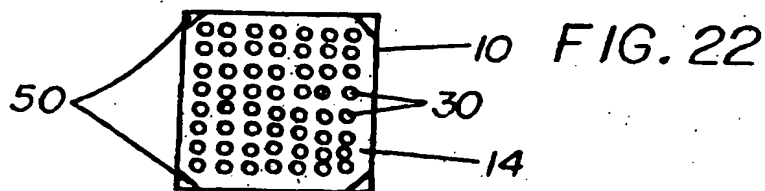
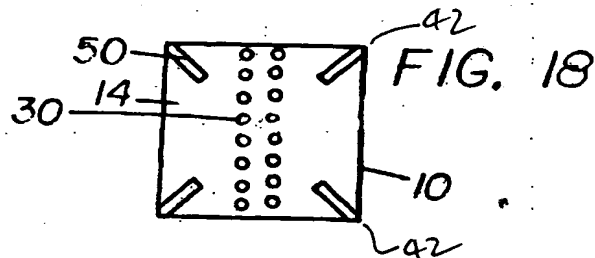
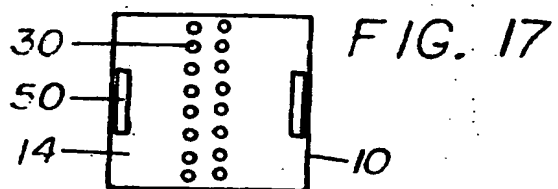
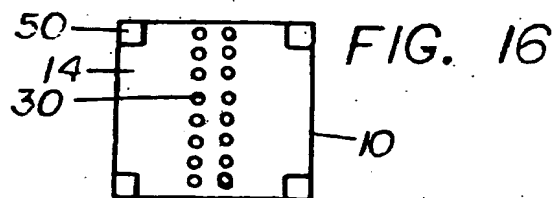
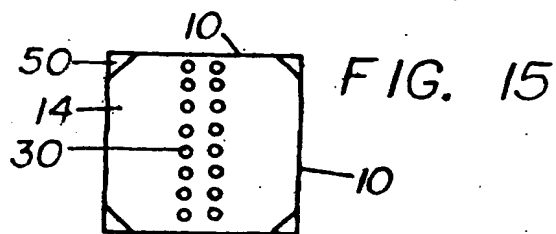
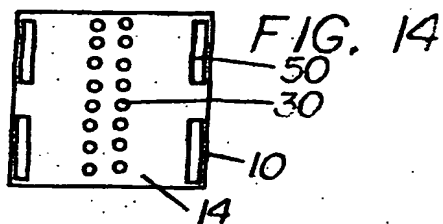
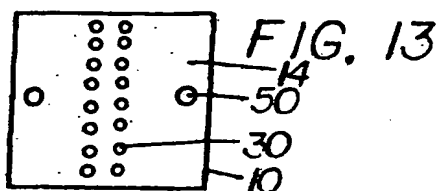
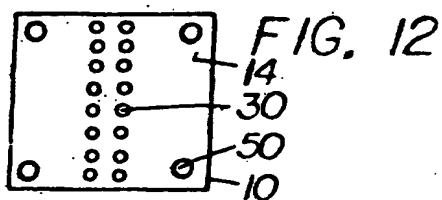
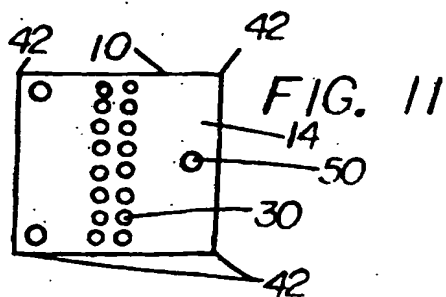


FIG. 10



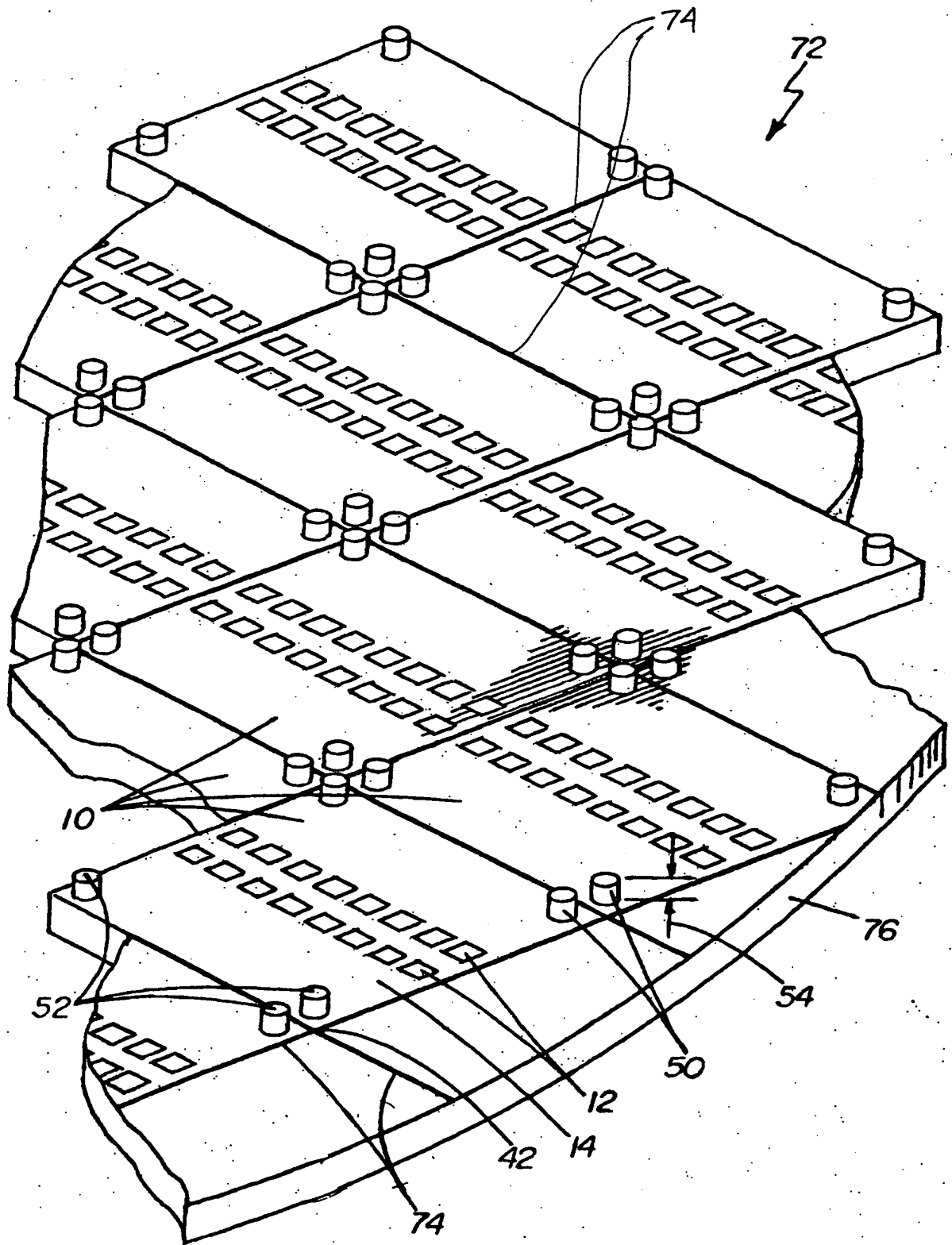
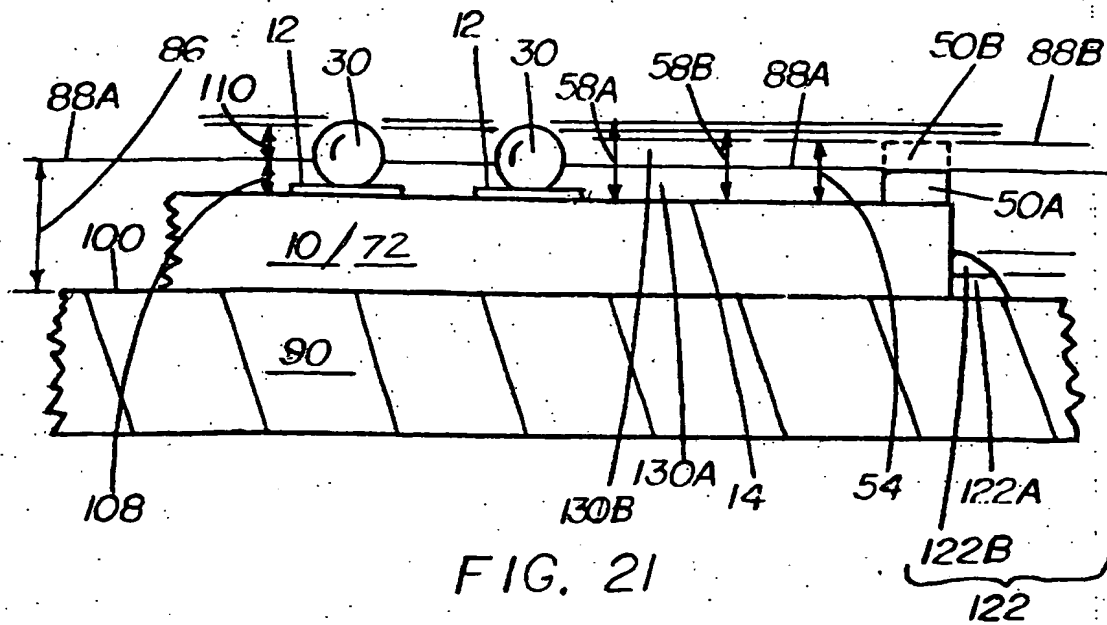


FIG. 19

FIG. 20



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